Intel

Compiler Engineer Intern

Type: Intern
Location: Brno

Work description:
As part of compiler engineering team, you will learn and contribute to the development of a compiler for the world-class 7nm, 12.8 Tbps Intel Tofino Ethernet switch chip, used in some of the biggest data centers of the world. The compiler is written in C++ and transforms the P4 language input program to run on highly optimized computational resources of Tofino. Compiler frontend is available at https://github.com/p4lang/p4c. You will work mainly with engineers in CZ, USA and UK.

Requirements:
- Hands-on experience with at least one object-oriented programming language, preferably C++
- Ongoing study in Electrical Engineering, Computer Science, or related field

You will learn:
- C++ programming: Design patterns, tree structures and traversal methods and more
- Working knowledge of related tools – cmake, ctest, gdb, git
- Unit testing
- Linux – bash, Python, Docker
- Modern compiler design
- Basic networking concepts and protocols
- Ability to cooperate within a team

Interested? Apply at jobs.intel.com or viktor.pus@intel.com.